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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10068148	FILING DATE 02/06/2002	CLASS 365	SUBCLASS	GAU 2818	EXAMINER <i>J.C. Soto</i>
**APPLICANTS: Lee Jae Goo.					
**CONTINUING DATA VERIFIED: <i>jk</i> NONE.					
** FOREIGN APPLICATIONS VERIFIED: <i>jk</i> REPUBLIC OF KOREA 2001-7276 02/14/2001					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials <i>JKM</i>		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 5649-952	
TITLE : Integrated circuit memory devices with per-bit redundancy and methods of operation thereof <i>JKM</i> COMM-FATA-TR-100-436.R2 02/06/02					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE		Application Examiner
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